ABSTRACT OF THE DISCLOSURE

A printed wiring board design aiding system comprises a first unit which acquires design layout information regarding a printed wiring board targeted for design, a second unit which acquires setting parameter information for the printed wiring board, which is targeted for design, and a third unit which estimates a value of the thickness of each of insulation layers of the printed wiring board in a post-manufacture state in accordance with the information acquired by the first unit and the information acquired by the second unit, and for outputting an estimated value.

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